

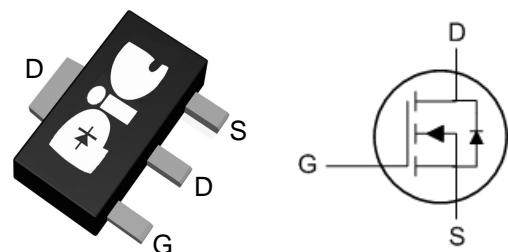
## ➤ General Description

This PAN60TK14K N-Channel enhancement mode power field effect transistor is the high density trench technology and this advanced technology can provide excellent Rds(On) performance and efficiency for power switching and load switching application., this device also comply with the RoHS and Green Product requirement with full function reliability approved.

## ➤ Feature

- Super high density cell design for extremely
- low RDS (ON)
- SOT-89-3L package design

## ➤ SOT-89-3L



## ➤ Application

- Motor and Load Control
- LCD TV Inverter & AD/DC Inverter Systems.
- Backlight Inverter for LCD Display
- Load Switch
- CCFL Inverter

## ➤ Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	I <sub>D</sub> @T <sub>A</sub> =25°C	5	A
Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	I <sub>D</sub> @T <sub>A</sub> =70°C	3.5	A
Pulsed Drain Current <sup>2</sup>	I <sub>DM</sub>	20	A
Single Pulse Avalanche Energy <sup>3</sup>	EAS	22	mJ
Avalanche Current	I <sub>AS</sub>	21	A
Total Power Dissipation <sup>4</sup>	P <sub>D</sub> @T <sub>A</sub> =25°C	2	W
Storage Temperature Range	T <sub>STG</sub>	-55 to 150	°C
Operating Junction Temperature Range	T <sub>J</sub>	-55 to 150	°C
Thermal Resistance Junction-ambient <sup>1</sup>	R <sub>θJA</sub>	62.5	°C/W

➤ **Electrical Characteristics (T<sub>A</sub>=25°C Unless otherwise noted)**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	60	---	---	V
Static Drain-Source On-Resistance <sup>2</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V , I <sub>D</sub> =4A	---	40	50	mΩ
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =3A	---	45	60	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	---	2.5	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =48V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =48V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C	---	---	5	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V	---	---	±100	nA
Forward Transconductance	g <sub>f</sub>	V <sub>DS</sub> =5V , I <sub>D</sub> =4A	---	28.3	---	S
Total Gate Charge (10V)	Q <sub>g</sub>	V <sub>DS</sub> =48V , V <sub>GS</sub> =10V , I <sub>D</sub> =4A	---	19	---	nC
Gate-Source Charge	Q <sub>gs</sub>		---	2.6	---	
Gate-Drain Charge	Q <sub>gd</sub>		---	4.1	---	
Turn-On Delay Time	T <sub>d(on)</sub>	V <sub>DD</sub> =30V , V <sub>GS</sub> =10V , R <sub>G</sub> =3.3Ω I <sub>D</sub> =4A	---	3	---	ns
Rise Time	T <sub>r</sub>		---	34	---	
Turn-Off Delay Time	T <sub>d(off)</sub>		---	23	---	
Fall Time	T <sub>f</sub>		---	6	---	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz	---	1027	---	pF
Output Capacitance	C <sub>oss</sub>		---	65	---	
Reverse Transfer Capacitance	C <sub>rss</sub>		---	46	---	

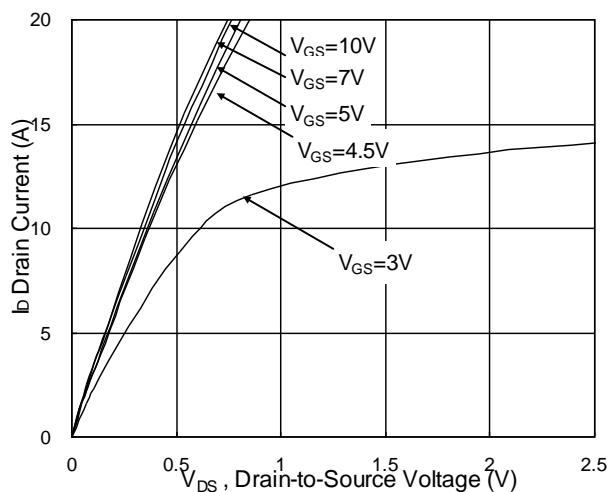
➤ **Diode Characteristics**

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Continuous Source Current <sup>1,5</sup>	I <sub>S</sub>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current	---	---	5	A
Diode Forward Voltage <sup>2</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C	---	---	1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =4A , dI/dt=100A/μs , T <sub>J</sub> =25°C	---	12.1	---	nS
Reverse Recovery Charge	Q <sub>rr</sub>		---	6.7	---	nC

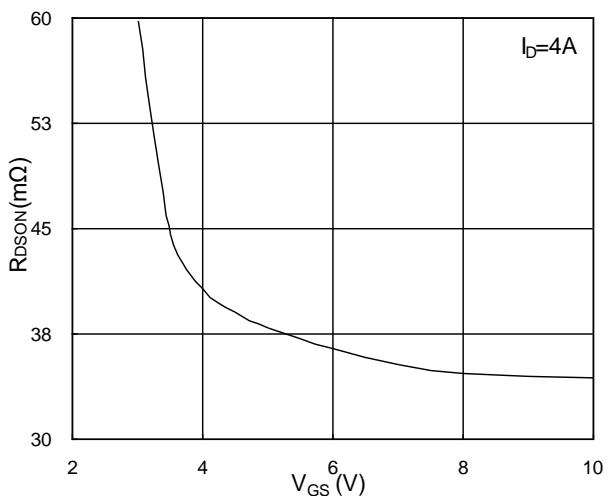
Note :

- 1.Pulse width limited by maximum junction temperature.
- 2.The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%
- 3.The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=25V,V<sub>GS</sub>=10V,L=0.1mH,I<sub>AS</sub>=21A
- 4.Ensure that the channel temperature does not exceed 150°C.
- 5.The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

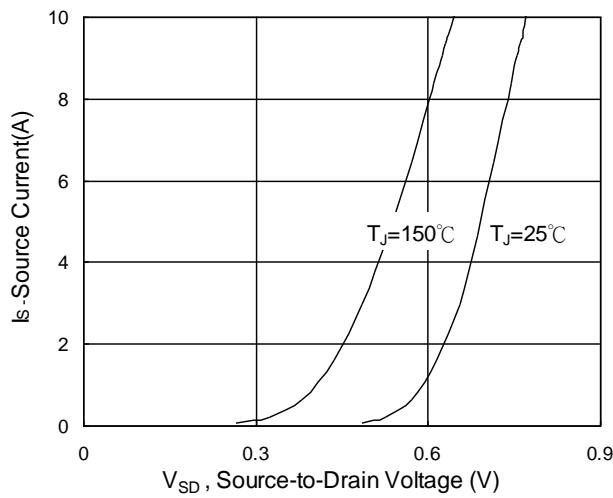
## ➤ Typical Characteristics



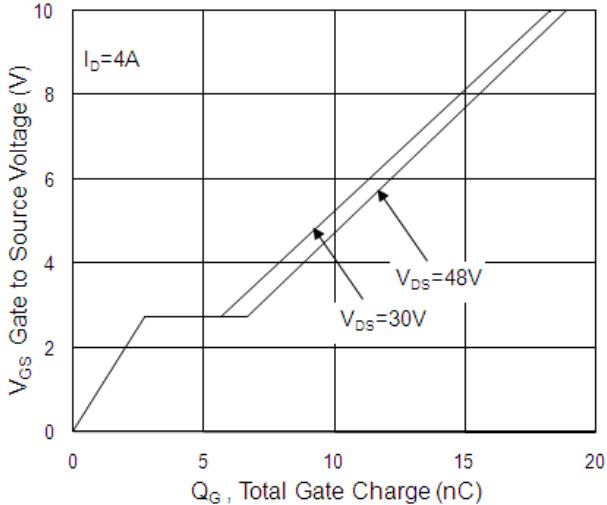
**Fig.1 Typical Output Characteristics**



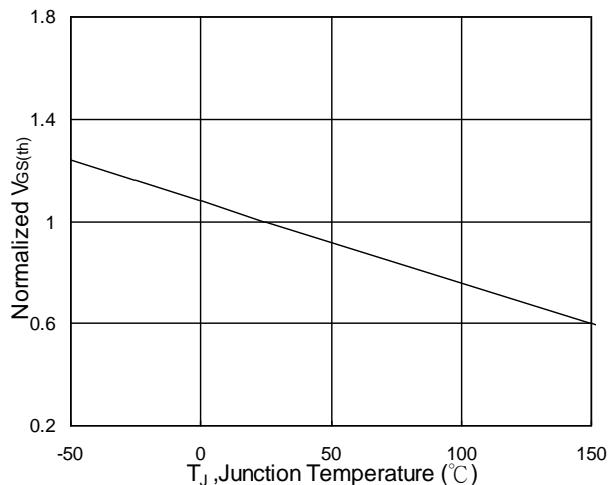
**Fig.2 On-Resistance vs G-S Voltage**



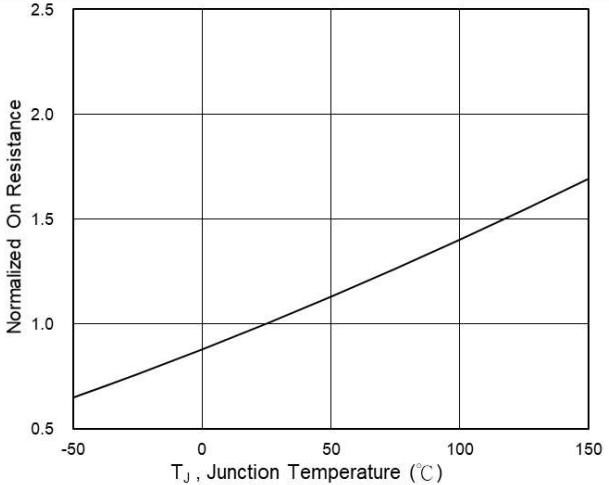
**Fig.3 Source Drain Forward Characteristics**



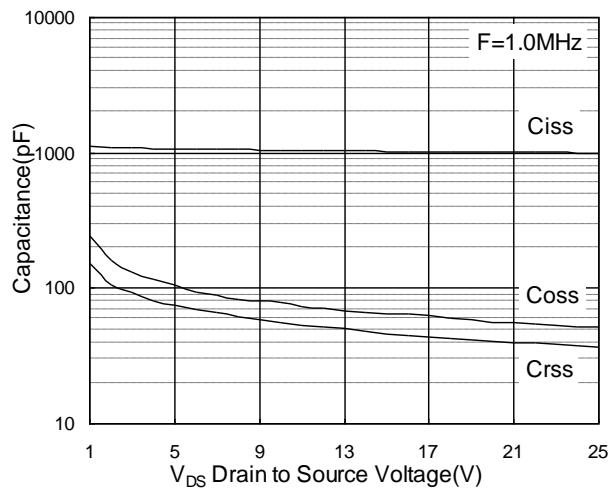
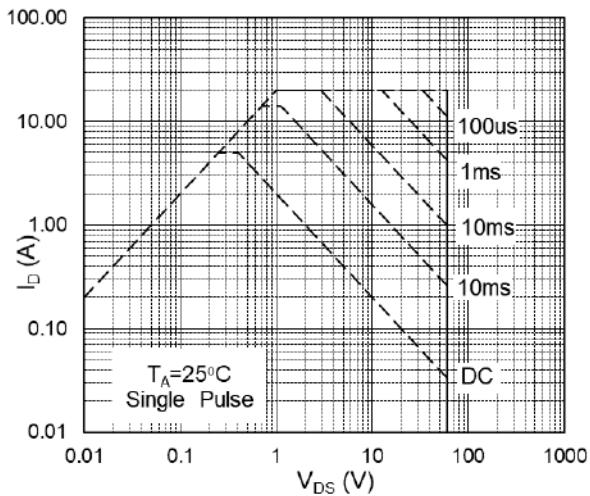
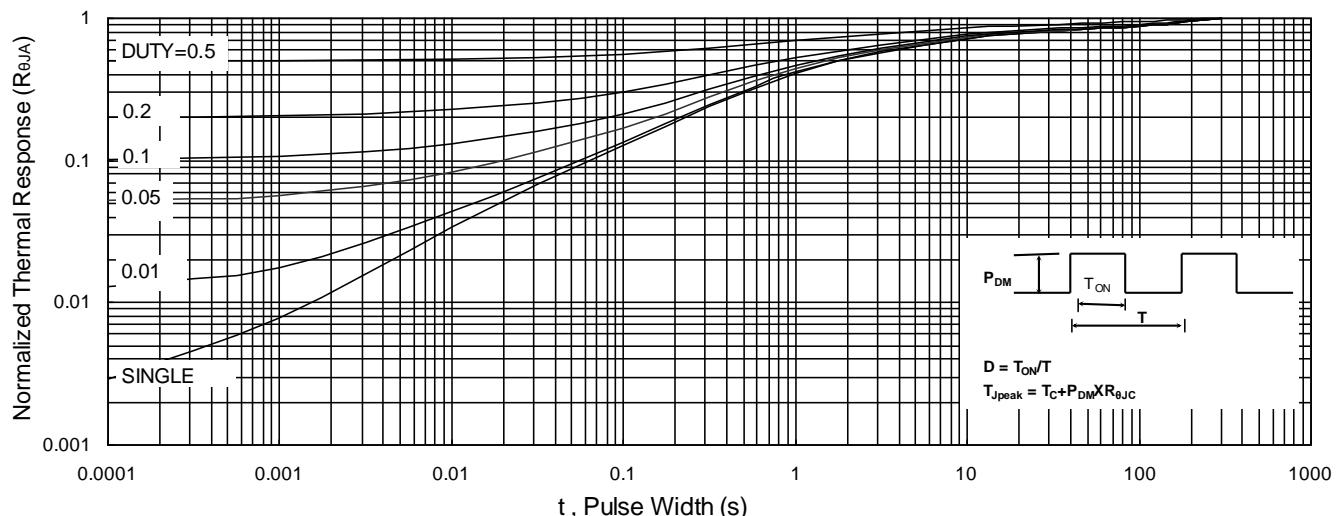
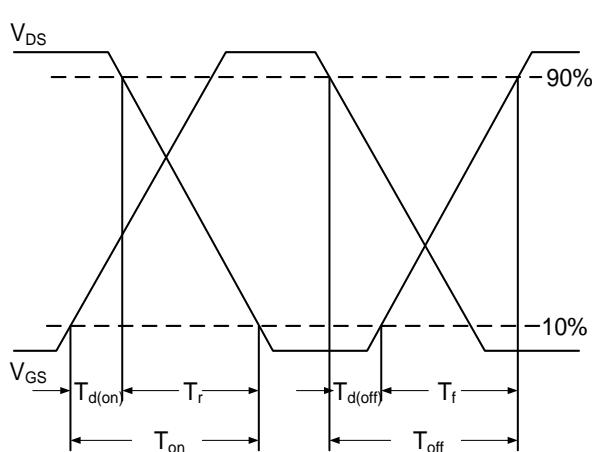
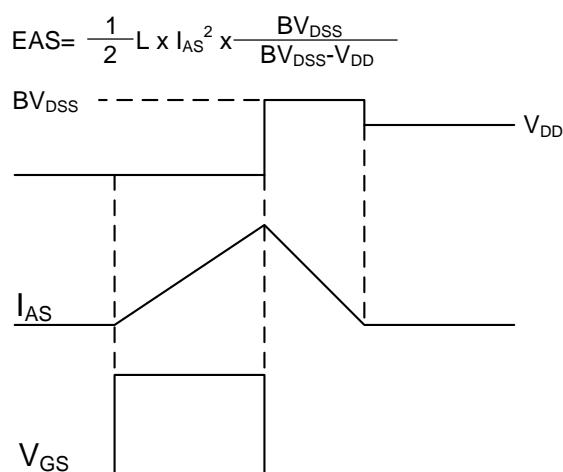
**Fig.4 Gate-Charge Characteristics**



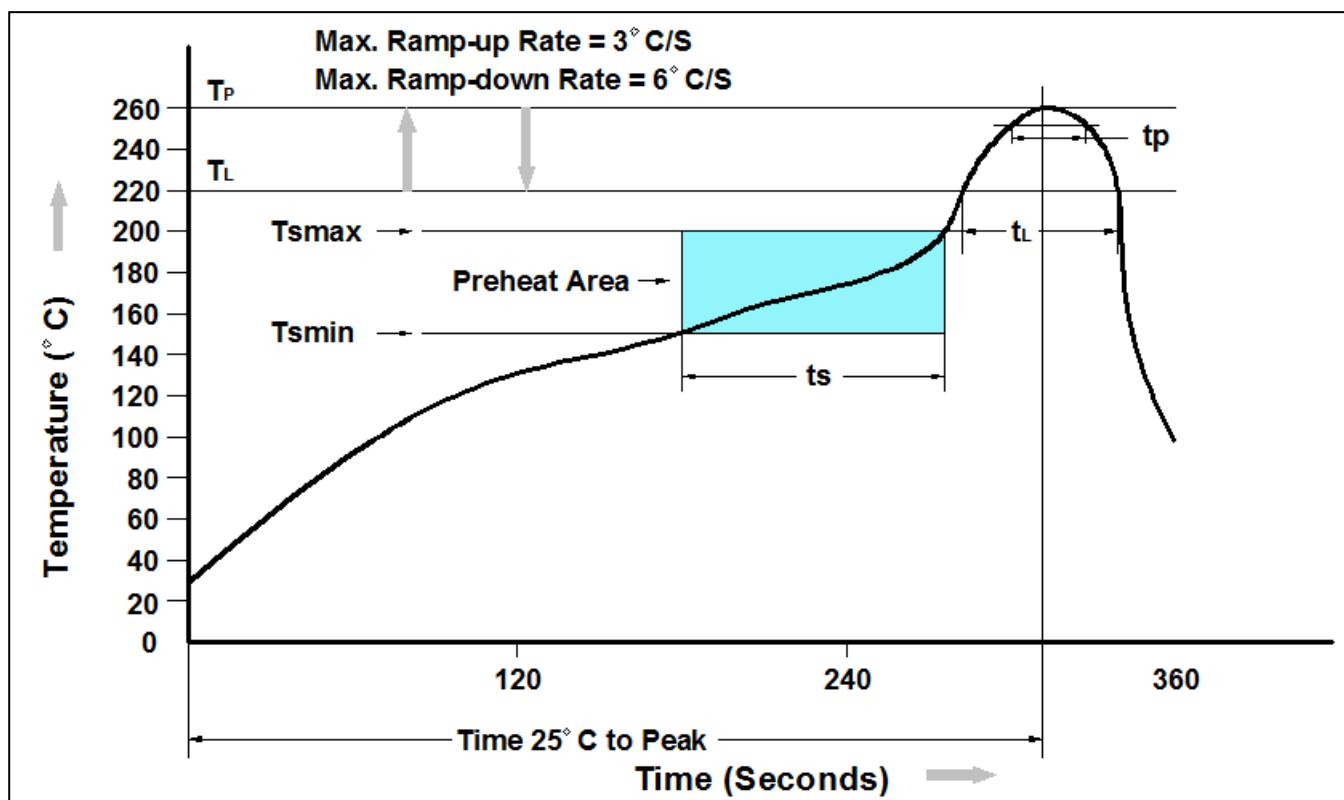
**Fig.5 Normalized  $V_{GS(th)}$  vs  $T_J$**



**Fig.6 Normalized  $R_{DS(on)}$  vs  $T_J$**


**Fig.7 Capacitance**

**Fig.8 Safe Operating Area**

**Fig.9 Normalized Maximum Transient Thermal Impedance**

**Fig.10 Switching Time Waveform**

**Fig.11 Unclamped Inductive Switching Waveform**

➤ **Recommand IR Reflow Soldering Thermal Profile**

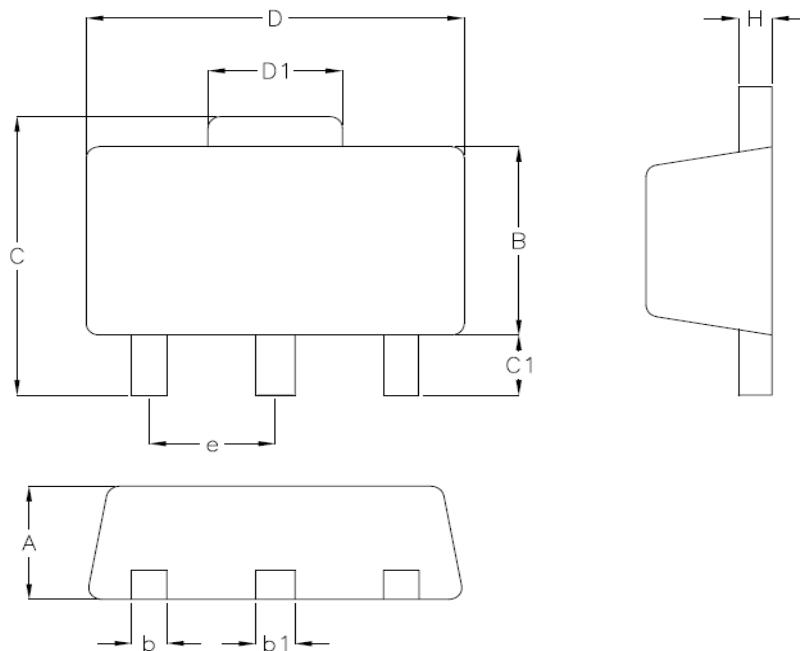


Profile Feature	Pb-Free Assembly Profile
Temperature Min. (T <sub>smin</sub> )	150°C
Temperature Max. (T <sub>smax</sub> )	200°C
Time (t <sub>s</sub> ) from (T <sub>smin</sub> to T <sub>smax</sub> )	60-120 seconds
Average Ramp-up Rate (t <sub>L</sub> to t <sub>P</sub> )	3°C/second max.
Liquidous Temperature (T <sub>L</sub> )	217°C
Time (t <sub>L</sub> ) Maintained Above (T <sub>L</sub> )	60 – 150 seconds
Peak Temperature	260°C +0°C / -5°C
Time (t <sub>P</sub> ) within 5°C of actual Peak Temperature	30 seconds
Ramp-down Rate (T <sub>P</sub> to T <sub>L</sub> )	6°C/second max
Time 25°C to Peak Temperature	8 minutes max.

➤ **Ordering Information**

Part Number	Description	Quantity
PAN60TK14K	SOT-89-3L Reel	1000 pcs

## ➤ Package Information (SOT-89-3L)



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.397	1.600	0.055	0.063
b	0.420	0.540	0.017	0.021
b1	0.420	0.540	0.017	0.021
B	2.388	2.591	0.094	0.102
C	3.937	4.242	0.155	0.167
C1	0.787	1.194	0.031	0.047
D	4.394	4.597	0.173	0.181
D1	1.397	1.753	0.055	0.069
e	1.448	1.549	0.057	0.061
H	0.350	0.44	0.014	0.017

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