

General Description

The PAE5V0LT is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space comes at a premium.

It has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by ESD(electrostatic discharge), and EFT (electrical fast transients).

Feature

- Peak Power Dissipation 50 W (8 x 20 us Waveform)
- Replacement for MLV (0402)
- ●Protects I/O Port
- ●Low Clamping Voltage
- ●Low Leakage
- \bullet Response Time is < 1 ns
- ●Stand-off Voltage: 5.0 V
- Solid-state silicon avalanche technology
- ROHS compliant
- Device Meets MSL 1 Requirements

Application

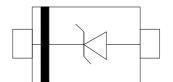
- Cellular handsets and accessories
- Portable instrumentation
- Peripherals
- Serial and Parallel Ports
- Notebooks, Desktops, Servers
- Projection TV

Protection solution to meet

- ●IEC61000-4-2 (ESD) ±15kV (air), ±8kV (contact)
- ●IEC61000-4-4 (EFT) 40A (5/50ns)









➤ Maximum Ratings (T_A=25°C Unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (tp=8/20μs waveform)	Ррр	50	Watts
ESD Rating per IEC61000-4-2: Contact		8	KV
Air		15	K V
Lead Soldering Temperature	$T_{ m L}$	260 (10 sec.)	${\mathbb C}$
Operating Temperature Range	Tı	-55 ~ 150	${\mathbb C}$
Storage Temperature Range	Tstg	-55 ~ 150	${\mathbb C}$
Lead Solder Temperature – Maximum (10 Second Duration)	TL	260	$^{\circ}$

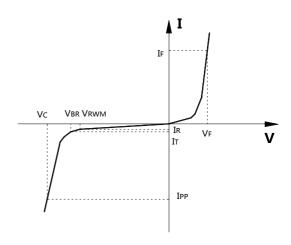
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

Electrical Characteristics (TA=25°C Unless otherwise specified)

V _{RWM}	V	v I @ v	V _{BR} @ 1 mA	$\mathbf{v_c}$	Capacitance	
	I _R @ V _{RWM}	(Volts)	@ 1 A	@ $V_R = 0 V, 1 MHz (pF)$		
	(V)	(uA)	Min	(V)	Тур	Max
PAE5V0LT	5.0	2	6.0	12	0.5	1

Junction capacitance is measured in VR=0V,F=1MHz

Symbol	Parameter
Vrwm	Working Peak Reverse Voltage
V_{BR}	Breakdown Voltage @ IT
$V_{\rm C}$	Clamping Voltage @ IPP
I_{T}	Test Current
Irm	Leakage current at VRWM
Ірр	Peak pulse current
Co	Off-state Capacitance
C_{J}	Junction Capacitance

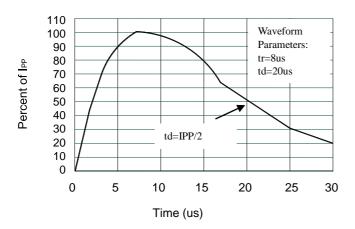


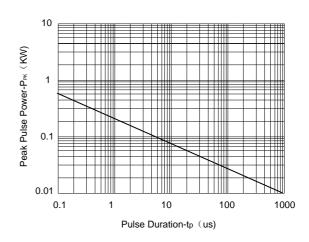
^{*}Other voltages may be available upon request.

^{1.} Non-repetitive current pulse, per Figure 1.



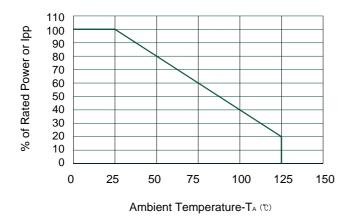
> Typical Characteristics





Pulse Waveform

Non-Repetitive Peak Pulse Power vs. Pulse Time

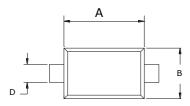


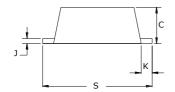
Power Derating Curve



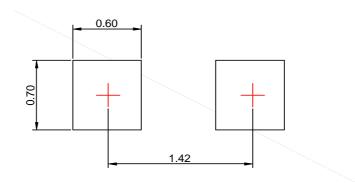
Package Information (SOD-523)

Case Material: Molded Plastic. UL Flammability





Dim	Millimeters		
	Min	Max	
A	1.10	1.30	
В	0.75	0.85	
С	0.51	0.70	
D	0.25	0.35	
J	0.08	0.15	
K	0.15	0.25	
S	1.50	1.70	



Recommended Pad outline

Ordering Information

Part Number	Description	Quantity
PAE5V0LT	SOD-523 Reel	3000 pcs





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