

PAE3351EU

Single-Line ESD Protection Array

General Description

The PAE3351EU is designed with latest technology to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space comes at a premium.

It has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by ESD(electrostatic discharge), and EFT (electrical fast transients).

Feature

- ●Single-channel ESD protection
- ●Peak Power Dissipation –60W (8 x 20 us Waveform)
- •Replacement for MLV (0402.0603)
- Protects I/O Port
- •Ultralow capacitance 0.3pf
- ●Low Clamping Voltage
- ●Low Leakage
- Response Time is < 1 ns
- •Stand-off Voltage: 3.3 V
- **•**RoHS Compliant
- •Meets MSL 1 Requirements
- •Reliable silicon device avalanche breakdown Structure

> <u>Application</u>

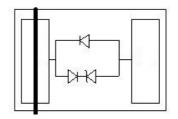
- •Cell phone handsets and accessories
- •Personal Digital Assistants (PDAs)
- •Portable Instrumentation
- Digital Cameras

Protection solution to meet

●IEC61000-4-2 (ESD) ±30 kV (contact), ±30kV (air) ●IEC61000-4-4 (EFT) 40A (5/50ns)

DFN-1006







Single-Line ESD Protection Array

Maximum Ratings (TA=25°C Unless otherwise specified)

Parameter	Symbol	Value	Unit	
ESD Rating per IEC61000-4-2: Contact		30	KN.	
Air		30	KV	
Lead Soldering Temperature	T _L	260 (10 sec.)	°C	
Operating Temperature Range	TJ	-55 ~ 125	°C	
Storage Temperature Range	T _{STG}	-55 ~ 150	°C	

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

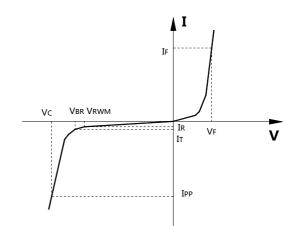
*Other voltages may be available upon request.

Electrical Characteristics (TA=25°C Unless otherwise specified)

		I _R @ V _{RWM}		V _{BR} @1 mA	Vc	P _{PPP}	I _{PP}	Capacitance	
Device	V _{RWM}	•	(Volts)	@ 1 A	tp=8/20µs	tp=8/20µs	@ V _R	= 0 V,	
Device		(uA)	A)	(vons)	WIA	waveform	waveform	1 MH	z (pF)
	(V)	Min	Max	Min	Max (V)	Max(W)	MAX(A)	Тур	Max
PAE3351EU	3.3	0.01	1.00	4	7.5	60	5	0.3	0.6

Junction capacitance is measured in $V_R=0V, F=1MHz$

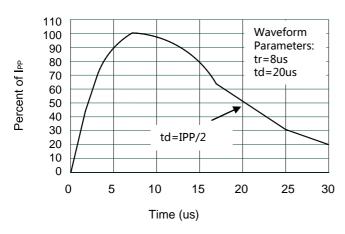
Symbol	Parameter		
Vrwm	Working Peak Reverse Voltage		
VBR	Breakdown Voltage @ IT		
V _C	Clamping Voltage @ IPP		
I _T	Test Current		
Irm	Leakage current at VRWM		
Ipp	Peak pulse current		
Co	Off-state Capacitance		
CJ	Junction Capacitance		

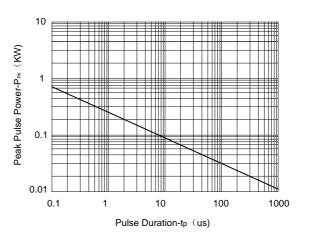




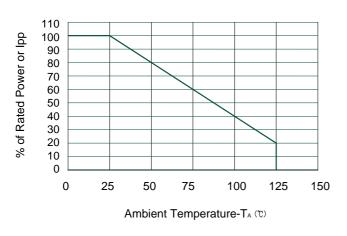
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Typical Characteristics





Non-Repetitive Peak Pulse Power vs. Pulse Time



Pulse Waveform

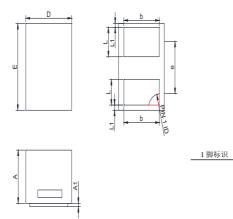
Power Derating Curve



Single-Line ESD Protection Array

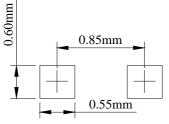
Package Information (DFN1006)

Case Material: Molded Plastic. UL Flammability

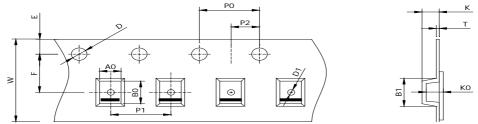


DIM	Millimeters			
	Min	Max		
Α	0.37	0.50		
A1	0.00	0.05		
D	0.55	0.65		
Ε	0.95	1.05		
b	0.25	0.60		
e	0.65TYP			
L	0.15 0.35			
L1	0.05REF			

Recommended Pad outline



DFN1006 Reel Dim



Destrome	Chip Size	Pocket Size	Tape	Reel Diameter	Quantity Per Reel	PO	P1
Package	(mm)	B0×A0×K0 (mm)	Width	Keel Diameter	Quantity Fer Keel		
DFN1006	$1.0 \times 0.6 \times 0.50$	1.10×0.70×0.60	8mm	178mm(7")	5000/10000	4mm	2mm
DO	D1	Е	F	К	Т	V	V
1.5mm	0. 5mm	1.75mm	3.5mm	0.55mm	0. 2mm	8mm	

Ordering Information

Part Number	Description	Quantity
PAE33511EU	DFN1006 Reel	10000 pcs



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