

General Description

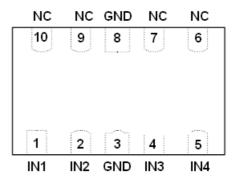
The PAE1524P is a 4-channel ultra-low capacitance rail clamp ESD protection diode array. Each channel consists of a pair of ESD diodes to direct positive or negative ESD current to either positive or negative rail. Zener diodes are integrated between the positive and negative power rails of the array. In a typical application, the negative rail pin (assigned as GND) is connected to system ground. Positive ESD current flows through the ESD ground, diode and Zener diode and the positive ESD voltage is clamped to the Zener voltage.

The PAE1524P is ideal for protecting high-speed data.

Feature

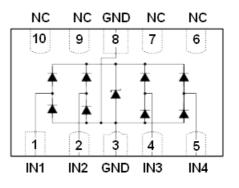
- 4 channels of ESD protection
- Provides ESD protection to IEC61000-4-2 level 4 ±15 KV air discharge
 - \pm 8 KV contact discharge
- Channel I/O to GND capacitance: 0.6pF(Max)
- Channel I/O to I/O capacitance: 0.45pF(Max)
- Low clamping voltage
- 5V low operating voltage
- Improved Zener structure
- Optimized package for easy high speed data lines PCB layout
- ROHS compliant





Application

- High Definition Multi-Media Interface Protection
- USB 3.0 Power and Data Line Protection
- Monitors and Flat Panel Displays Notebook Computers
- Video Line Protection & Base Stations
- HDSL, IDSL Secondary IC Side Protection
- Microcontroller Input Protection
- LCD and camera modules
- 10/100/1000 Ethernet





Maximum Ratings (TA=25°C Unless otherwise specified)

Parameter	Symbol	Typical	Unit
Peak Pulse Power ($t_p = 8/20 \ \mu s$)	P _{pk}	150	W
Peak Pulse Current ($t_p = 8/20 \ \mu s$)	Ipp	5	А
ESD per IEC 61000 – 4 – 2 (Air)	Vesd1	±15	KV
ESD per IEC 61000 – 4 – 2 (Contact)	Vesd2	±8	KV
Operating Junction Temperature	Τı	-55 ~ 125	°C
Storage Temperature Range	Tstg	-55 ~ 150	°C

Electrical Characteristics (TA=25°C Unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур	Max.	Unit
Reverse Working Voltage	VRWM	Any Pin to GND			5	V
Forward Voltage	VF	IF =10mA	0.4	0.8	1.5	V
Reverse Breakdown Voltage	VBR	$I_t = 1mA$ Any Pin to GND	6			V
Reverse Leakage Current	Ir	$V_{RWM} = 5V$, T=25k Any Pin to GND		0.03	1	μA
Positive Clamping Voltage	Vci	IPP = 1A, tp = $8/20 \ \mu s$ Positive pulse Any Pin to GND		8.5	12	v
Negative Clamping Voltage	Vc2	$I_{PP} = 1A$, tp = 8/20 µs Negative pulse Any Pin to GND		1.8		v
Junction Capacitance Between Channel	Cj1	$V_R = 0V$, f = 1MHz Between I/O Pin		0.35	0.45	pF
Junction Capacitance Between I/O to GND	Cj2	$V_R = 0V$, f = 1MHz Any Pin to GND		0.5	0.6	pF



Electrical Parameter

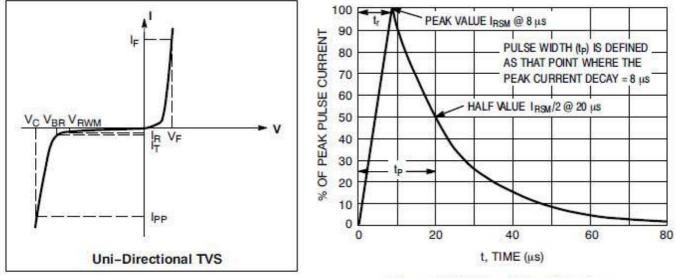
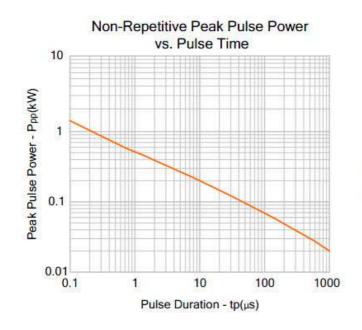


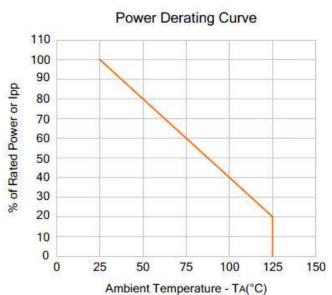
Figure 1.	8 X 20 µs	Pulse	Waveform
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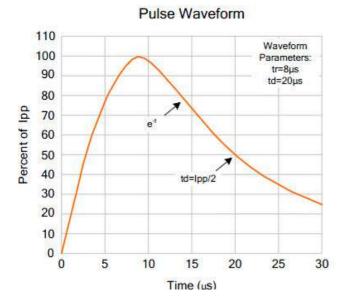
Symbol	Parameter	
I _{PP}	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @ IPP	
V _{RWM}	Working Peak Reverse Voltage	
l _R	Maximum Reverse Leakage Current @ VRWM	
l _T	Test Current	
V _{BR}	Breakdown Voltage @ IT	
I _F	Forward Current	
VF	Forward Voltage @ IF	

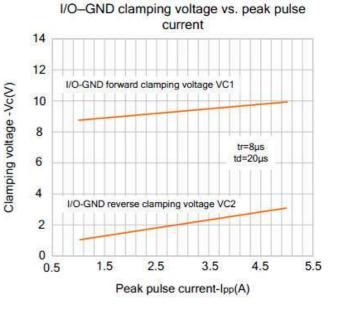


Typical Characteristics











CH1

Couple

DC

Bandwidth

limit Off 60MHz

V/Grid

Rough

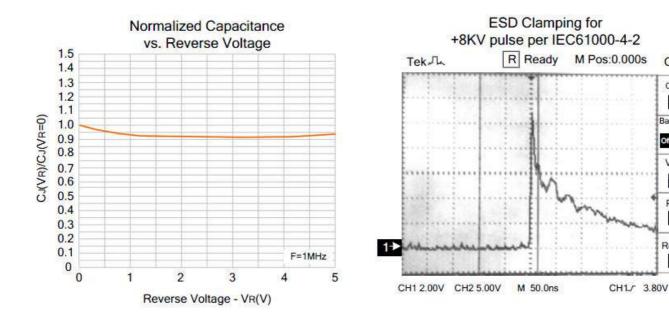
Probe

1X

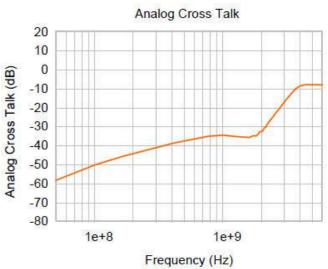
Reverse

Off

Typical Characteristics





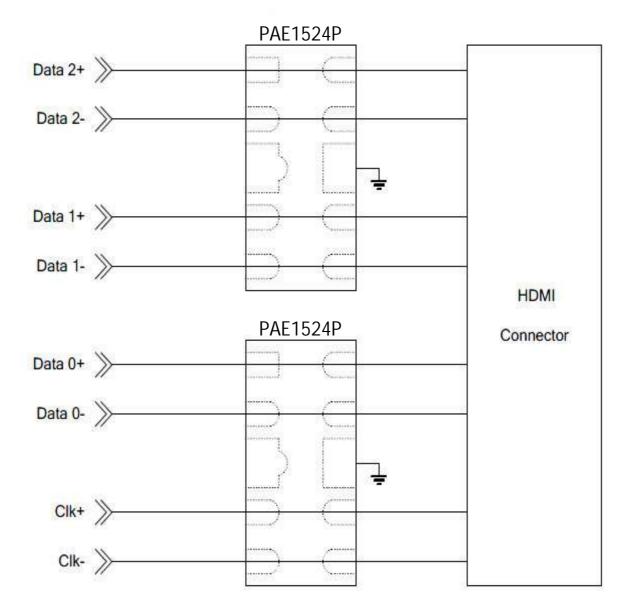




PAE1524P

Ultra Low Capacitance 4-Channel ESD Protection Array

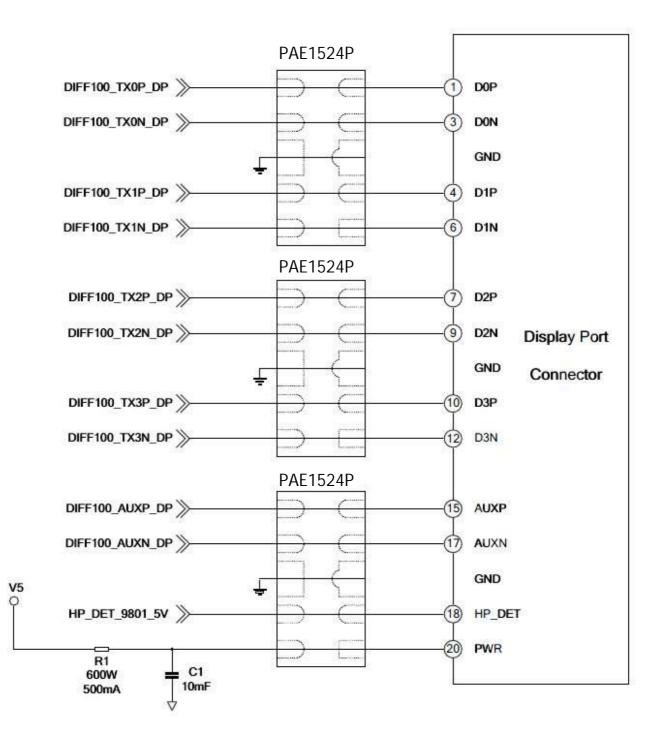






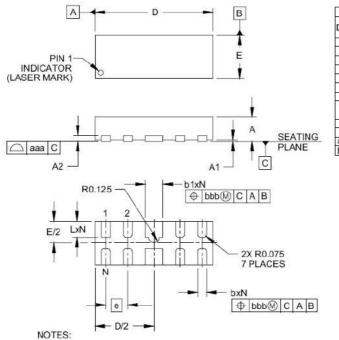
Application Information \geq

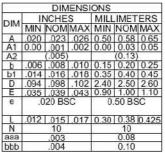
(Display Port Application)



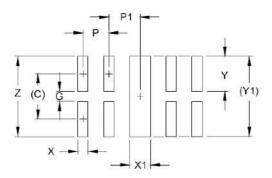


Package Information (DFN-10)





1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).



DIMENSIONS			
DIM	INCHES	MILLIMETERS	
С	(.034)	(0.875)	
G	.008	0.20	
P	.020	0.50	
P1	.020	0.50	
X	.008	0.20	
X1	.016	0.40	
Y	.027	0.675	
Y1	(.061)	(1.55)	
Z	.061	1.55	

NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY.

CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

Ordering Information

Part Number	Description	Quantity
PAE1524P	DFN-10 Reel	3000 pcs



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