

## General Description

The PAE05VEB device is characterized by their ultra low capacitance, low operating and clamping voltages, and fast response time. This makes it ideal for use as board level protection of sensitive semiconductor components. The dual-junction common-anode design allows the user to protect two bidirectional lines.

It has been specifically designed to protect sensitive components which are connected to data and transmission lines from overvoltage caused by ESD(electrostatic discharge), CDE (Cable Discharge Events), and EFT (electrical fast transients).

### Feature

- ●Ultra small package(DFN1006) for use in portable electronics
- ●ESD protection of two lines
- ●Ultra Low leakage current
- ●Ultra Low capacitance: C<sub>J</sub>=0.35pF typ
- lacktriangle Response Time is < 1 ns
- Working voltages :5.0V
- ●Solid-state silicon avalanche technology
- Device Meets MSL 1 Requirements
- ROHS compliant

# Application

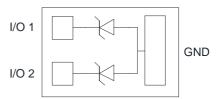
- ●USB1.1/2.0/3.0/3.1 Data lines
- ●HDMI 2.0
- ●Industrial Controls
- •Computers and peripherals
- Portable instrumentation
- ■Notebook Computers
- **●**DVI
- Projection TV
- Audio and video equipment
- Subscriber Identity Module (SIM) card protection

# Protection solution to meet

- ●IEC61000-4-2 (ESD): ±15kV (air), ±8kV (contact)
- ●IEC61000-4-4 (EFT): 40A (5/50ns)

### > DFN-1006







### Maximum Ratings (TA=25°C Unless otherwise specified)

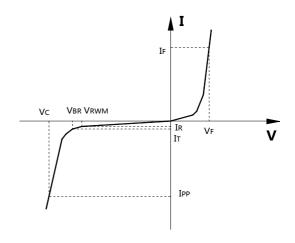
Parameter	Symbol	Value	Unit
ESD Rating per IEC61000-4-2: Contact		20	KV
Air		20	K V
Lead Soldering Temperature	$T_{ m L}$	260 (10 sec.)	°C
Operating Temperature Range	Τı	-55 ~ 150	°C
Storage Temperature Range	Tstg	-55 ~ 150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simul taneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

# **► Electrical Characteristics (TA=25°C Unless otherwise specified)**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
$V_{RWM}$	Reverse Working Voltage	any I/O pin to Ground	any I/O pin to Ground		5.0	V
$V_{BR}$	Reverse Breakdown Voltage	$I_T = 1 \mathrm{mA}$	6.0			V
$I_{RM}$	Reverse Leakage Current	V <sub>RWM</sub> =5V			0.1	uA
$V_{\mathrm{F}}$	Forward Voltage	$I_T = 15mA$		0.9	1.2	V
		Pin3 to Pin1、Pin2			1.2	
V	V. Clamaina Waltana	$I_{PP}=1A, t_{P}=8/20\mu s;$		8.4	9.8	V
$V_{\rm C}$	Clamping Voltage	$I_{PP}=2A, t_P=8/20\mu s;$		9.8	15	V
C	Junction Capacitance	$V_R = 0V$ , $f = 1MHz$		0.35	0.60	pF
$C_{J}$		Any I/O pin to Ground				

Symbol	Parameter		
Vrwm	Working Peak Reverse Voltage		
$V_{BR}$	Breakdown Voltage @ IT		
V <sub>C</sub>	Clamping Voltage @ IPP		
$I_T$	Test Current		
Irm	Leakage current at VRWM		
IPP	Peak pulse current		
Co	Off-state Capacitance		
$C_{\mathrm{J}}$	Junction Capacitance		

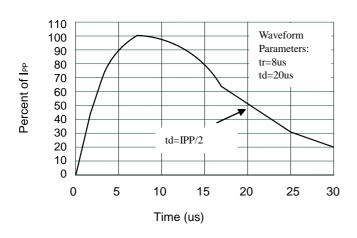


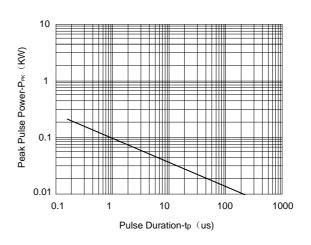
<sup>\*</sup>Other voltages may be available upon request.

<sup>1.</sup> Non-repetitive current pulse, per Figure 1.



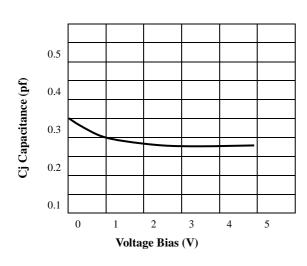
# Typical Characteristics

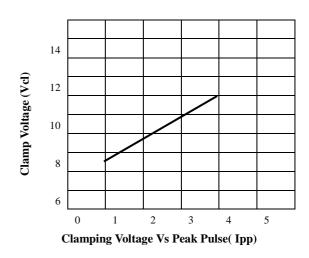


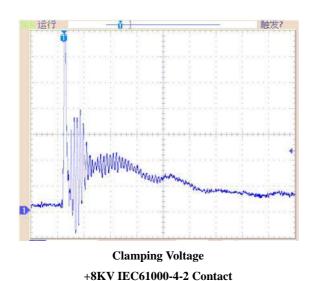


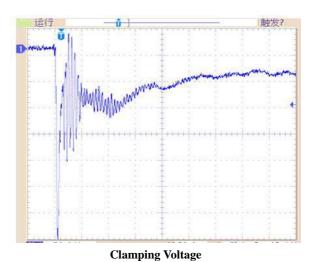
**Pulse Waveform** 









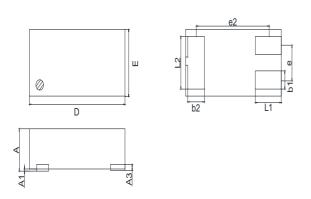


-8KV IEC61000-4-2 Contact



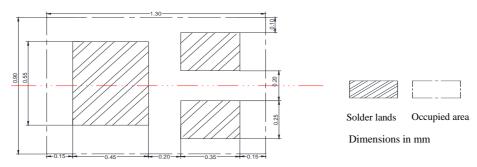
# Package Information (DFN1006)

Case Material: Molded Plastic. UL Flammability

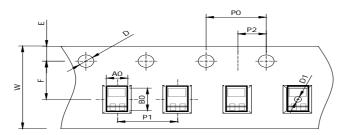


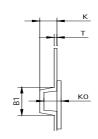
Dim	Millimeters		
Dini	Min	Max	
A	0.40	0.55	
A1	0.01	0.10	
A3	0.125REF		
D	0.95	1.05	
E	0.55	0.65	
b1	0.10	0.20	
b2	0.20	0.30	
L1	0.20	0.40	
L2	0.40	0.60	
e1	0.30	0.40	
e2	0.675BSC		

Pad Layout



#### DFN1006-3L Reel Dim





Package	Chip Size	Pocket Size	Tape	Reel Diameter	Quantity Per Reel	PO	P1
Гаскаде	(mm)	B0×A0×K0(mm)	Width	Reel Diameter	Quantity Fer Reer	10	11
DFN1006-3L	1.00×0.60×0.50	1.65×1.55×0.65	8mm	178mm(7")	10000	4mm	2mm
D0	D1	Е	F	К	T	V	V
1.5mm	-	1.75mm	3.5mm	0.60mm	0.3mm	n 8mm	

# Ordering Information

Part Number	Description	Quantity
PAE05VEB	DFN1006 Reel	10000 pcs





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