

## **PAE0524MU**

### Ultra Low Capacitance Array for ESD Protection

DFN2510-10L

### General Description

The PAE0524MU provides a typical line to line capacitance of 0.2pF between I/O pins and low insertion loss up to 3GHz providing greater signal integrity making it ideally suited for HDMI applications, such as Digital TVs, DVD players, Computing, set-top boxes and MDDI applications in mobile computing devices.

It has been specifically designed to protect sensitive components which are connected to high-speed data and transmission lines from overvoltage caused by ESD(electrostatic discharge), CDE (Cable Discharge Events), and EFT (electrical fast transients).

### Feature

- Protects two or four I/O lines
- Low capacitance:0.2pf Typical between I/O channel
- Working voltages : 5V
- Low leakage current
- Response Time is < 1 ns
- Meets MSL 1 Requirements
- Solid-state silicon avalanche technology
- ROHS compliant

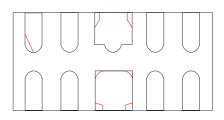
## > Application

- High Definition Multi-Media Interface (HDMI1.3/1.4/2.0)
- Digital Visual Interface (DVI)
- Display Port Interface
- Serial ATA
- PCI Express
- USB 1.1/2.0/3.0/3.1/OTG
- IEEE 1394 Firewire Ports
- Projection TV Monitors and Flat Panel Displays
- Notebook Computers
- Set Top Box
- Projection TV

## Protection solution to meet

- IEC61000-4-2 (ESD) ±20kV (air), ±20kV (contact)
- IEC61000-4-4 (EFT) 40A (5/50ns)





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10	9	8	7	6



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## Maximum Ratings (TA=25°C Unless otherwise specified)

Parameter	Symbol	Value	Unit		
Peak Pulse Power (tp=8/20µs waveform)	Рррр	55	Watts		
ESD Rating per IEC61000-4-2: Contact		20	<b>U</b> M		
Air		20	KV		
Lead Soldering Temperature	TL	260 (10 sec.)	°C		
Operating Temperature Range	τı	-55 ~ 150	°C		
Storage Temperature Range	Tstg	-55 ~ 150	°C		

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

\*Other voltages may be available upon request.

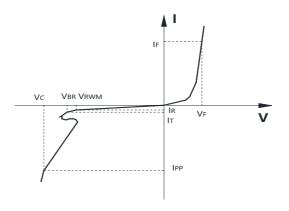
1. Non-repetitive current pulse, per Figure 1.

## Electrical Characteristics (TA=25°C Unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
$\mathbf{V}_{\text{rwm}}$	Reverse Working Voltage	Any I/O to Ground			5.0	V
$\mathbf{V}_{\scriptscriptstyle \mathrm{BR}}$	Reverse Breakdown Voltage	$I_T = 0.1 mA$ , Any I/O to Ground	6.0			v
$I_{\scriptscriptstyle R}$	Reverse Leakage Current	V <sub>RWM</sub> = 5V, Any I/O to Ground			1	μΑ
$\mathbf{V}_{\mathrm{F}}$	Diode Forward Voltage	$I_F = 15 m A$		0.85	1.2	V
V	Clampine Voltage	$I_{PP} = 1A^{(1)},$ any I/O pin to Ground			6	v
$V_{c}$	Clamping Voltage	$I_{PP} = 12A^{(1)},$ any I/O pin to Ground			12	v
R <sub>dyn</sub>	dynamic resistance	positive transient(TLP) negative transient(TLP)		0.26 0.28		Ω
C <sub>I</sub> <sup>(2)</sup>	Innotion Consultance	$V_{IN} = 2.5V$ , f = 1MHz, between I/O pins		0.20		pF
$C_{J}$	Junction Capacitance	$V_{IN}$ = 2.5V, f = 1MHz, any I/O pin to Ground		0.45	0.65	pF

Notes:(1)Measurements performed using a 100ns Transmission Line Pulse(TLP) system. (2)Junction capacitance is measured in VR=0V,F=1MHz

Symbol Parameter V<sub>RWM</sub> Working Peak Reverse Voltage  $V_{BR}$ Breakdown Voltage @ IT Clamping Voltage @ Ipp100ns V<sub>C</sub> Transmission Line Pulse(TLP)  $I_T$ Test Current  $I_{RM}$ Leakage current at VRWM  $\mathbf{I}_{PP}$ Peak pulse current  $C_0$ Off-state Capacitance Junction Capacitance  $C_J$ 

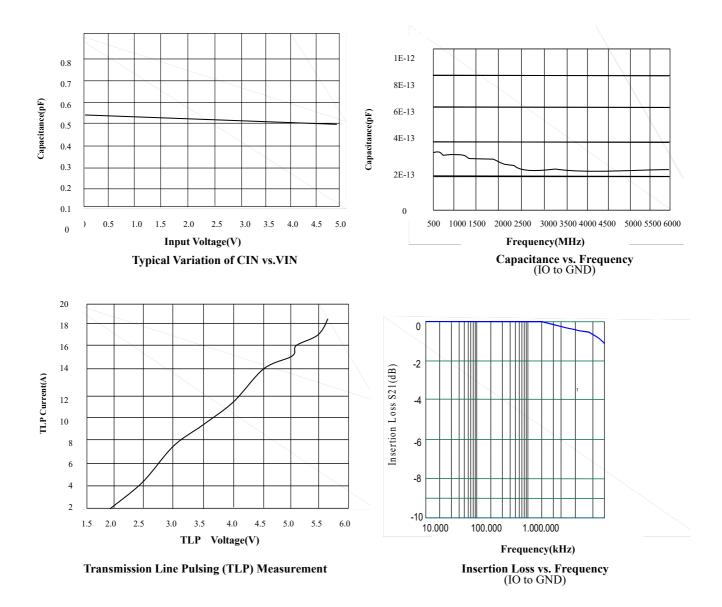




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## Typical Characteristics



Ordering Information

Part Number	Description	Quantity		
PAE0524MU	DFN2510-10L Reel	3000 pcs		



# **PAE0524MU**

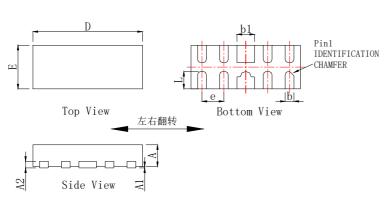
### Ultra Low Capacitance Array for ESD Protection

## Package Information (DFN2510-10L)

### **Mechanical Data**

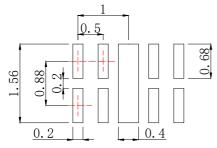
Case:DFN2510

Case Material: Molded Plastic. UL Flammability



DIM	Millimeters				
DIM	Min	Max			
А	0.45	0.65			
A1	0.05REF				
A2	0.15REF				
b	0.15 0.25				
b1	0.30	0.50			
D	2.424	2.576			
Е	0.924	1.076			
e	0.50REF				
L	0.30 0.45				

### **Recommended Pad outline**

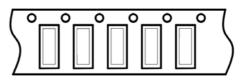


#### DFN2510 Reel Dim

#### Progressive direction ш P0 0 $( \square )$ $\oplus$ $\oplus$ K0 ш ≥ ⊕ 0 曲 • В A0 P1

PACKAGE	W	Е	F	PO	D	P2	P1	Т	A0	B0	K0
DFN2510	8mm	1.75mm	3.5mm	4mm	1.5mm	2mm	4mm	0.23mm	1.2 mm	2.7mm	0.8mm
	±0.1	±0.1	±0.05	±0.1	±0.1	±0.05	±0.1	±0.05	±0.05	±0.1	±0.05

## Device Orientation in Tape



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