

PAE0524M2

Ultra Low Capacitance Array for ESD Protection

DFN2510-10L

General Description

The PAE0524M2 provides a typical line to line capacitance of 0.15pF between I/O pins and low insertion loss up to 3GHz providing greater signal integrity making it ideally suited for HDMI applications, such as Digital TVs, DVD players, Computing, set-top boxes and MDDI applications in mobile computing devices.

It has been specifically designed to protect sensitive components which are connected to high-speed data and transmission lines from overvoltage caused by ESD(electrostatic discharge), CDE (Cable Discharge Events), and EFT (electrical fast transients).

Feature

- Protects two or four I/O lines
- Low capacitance:0.15pf Typical between I/O channel
- Working voltages : 5.5V
- Low leakage current
- Response Time is < 1 ns
- Meets MSL 1 Requirements
- Solid-state silicon avalanche technology
- ROHS compliant

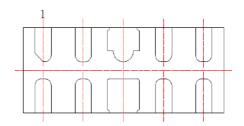
> <u>Application</u>

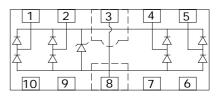
- High Definition Multi-Media Interface (HDMI1.3/1.4/2.0)
- Digital Visual Interface (DVI)
- Display Port Interface
- Serial ATA
- PCI Express
- USB 1.1/2.0/3.0/3.1/OTG
- IEEE 1394 Firewire Ports
- Projection TV Monitors and Flat Panel Displays
- Notebook Computers
- Set Top Box
- Projection TV

Protection solution to meet

- IEC61000-4-2 (ESD) ±20kV (air), ±20kV (contact)
- IEC61000-4-4 (EFT) 40A (5/50ns)
- IEC61000-4-5 (Lightning) 3.5A (8/20 μs)









Ultra Low Capacitance Array for ESD Protection

Maximum Ratings (TA=25°C Unless otherwise specified)

| Parameter | Symbol | Value | Unit | | |
|--|--------|---------------|-------|--|--|
| Peak Pulse Power (tp=8/20µs waveform) | Pppp | 45 | Watts | | |
| Peak Pulse Current(tp=8/20µs waveform) | Ipp | 3.5 | А | | |
| ESD Rating per IEC61000-4-2: Contact | | 20 | | | |
| Air | | 20 | KV | | |
| Lead Soldering Temperature | TL | 260 (10 sec.) | °C | | |
| Operating Temperature Range | τJ | -55 ~ 150 | °C | | |
| Storage Temperature Range | Тятд | -55 ~ 150 | °C | | |

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied ,damage may occur and reliability may be affected.

*Other voltages may be available upon request.

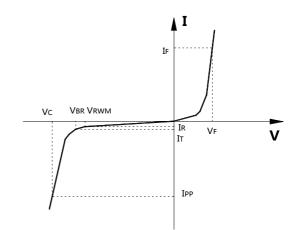
1. Non-repetitive current pulse, per Figure 1.

Electrical Characteristics (TA=25°C Unless otherwise specified)

| Symbol | Parameter | Conditions | Min. | Тур. | Max. | Units |
|-------------------------|---------------------------|-----------------------------|------|------|------|-------|
| VRWM | Reverse Working Voltage | Any I/O to Ground | | | 5.5 | V |
| VBR Reverse | Davana Draakdavin Valtaat | IT = 1mA, | 6.0 | | | v |
| | Reverse Breakdown Voltage | Any I/O to Ground | 0.0 | | | |
| Ir Rev | Devience Leeke as Current | $V_{RWM} = 5V$, | | | 0.5 | μΑ |
| | Reverse Leakage Current | Any I/O to Ground | | | 0.5 | |
| VF | Diode Forward Voltage | IF = 15mA | | 0.85 | 1.2 | V |
| Vc Clamping Voltage | | $I_{PP} = 1A$, tp =8/20µs, | | 8.6 | 9.8 | V |
| | Clamping Voltage | any I/O pin to Ground | | | | |
| | Clamping Voltage | $I_{PP} = 3A$, tp =8/20µs, | | 11.4 | 15 | V |
| | | any I/O pin to Ground | | | | |
| Rdyn dynamic resistance | demonsio registance | positive transient | | 0.48 | | Ω |
| | dynamic resistance | negative transient | | 0.35 | | 52 |
| Cr Junction Cap | | $V_R = 0V, f = 1MHz,$ | | 0.1 | 0.25 | pF |
| | Junction Consoltance | between I/O pins | | | | |
| | Junction Capacitance | $V_R = 0V$, $f = 1MHz$, | 0 | 0.36 | 0.6 | pF |
| | | any I/O pin to Ground | | 0.30 | 0.0 | pr. |

Junction capacitance is measured in VR=0V,F=1MHz

| Symbol | Parameter | |
|--------|------------------------------|--|
| Vrwm | Working Peak Reverse Voltage | |
| VBR | Breakdown Voltage @ IT | |
| Vc | Clamping Voltage @ IPP | |
| Іт | Test Current | |
| Irm | Leakage current at VRWM | |
| Ipp | Peak pulse current | |
| Со | Off-state Capacitance | |
| Cı | Junction Capacitance | |

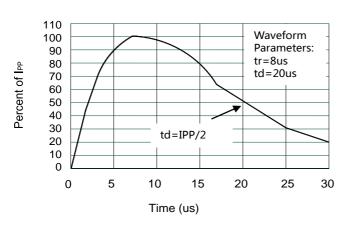


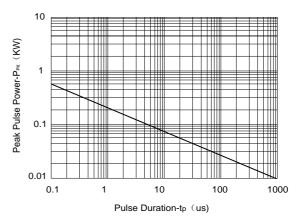


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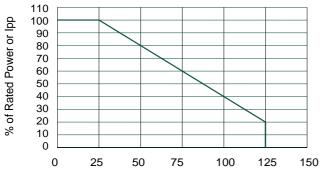
Typical Characteristics





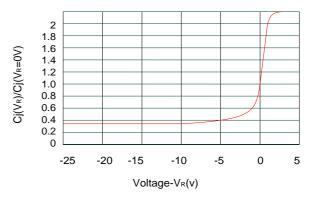
Pulse Waveform

Non-Repetitive Peak Pulse Power vs. Pulse Time

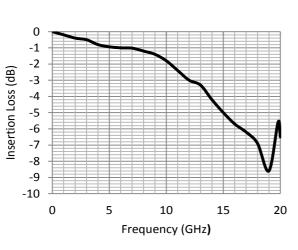


Ambient Temperature-T_A (°C)

Power Derating Curve



Junction Capacitance vs. Reverse Voltage



Insertion Loss S21



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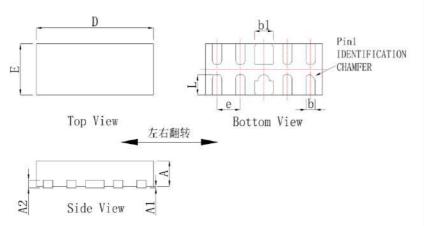
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Package Information (DFN2510-10L)

Mechanical Data

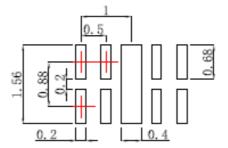
Case:DFN2510

Case Material: Molded Plastic. UL Flammability



| DIM | Millimeters | | |
|-----|-------------|-------|--|
| DIM | Min | Max | |
| А | 0.45 | 0.65 | |
| A1 | 0. 05REF | | |
| A2 | 0.15REF | | |
| b | 0.15 | 0.25 | |
| b1 | 0.30 | 0.50 | |
| D | 2.424 | 2.576 | |
| Е | 0.924 | 1.076 | |
| L | 0.30 | 0.45 | |
| е | 0.50 REF | | |

Recommended Pad outline



Ordering Information

| Part Number | Description | Quantity |
|-------------|------------------|----------|
| PAE0524M2 | DFN2510-10L Reel | 3000 pcs |



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